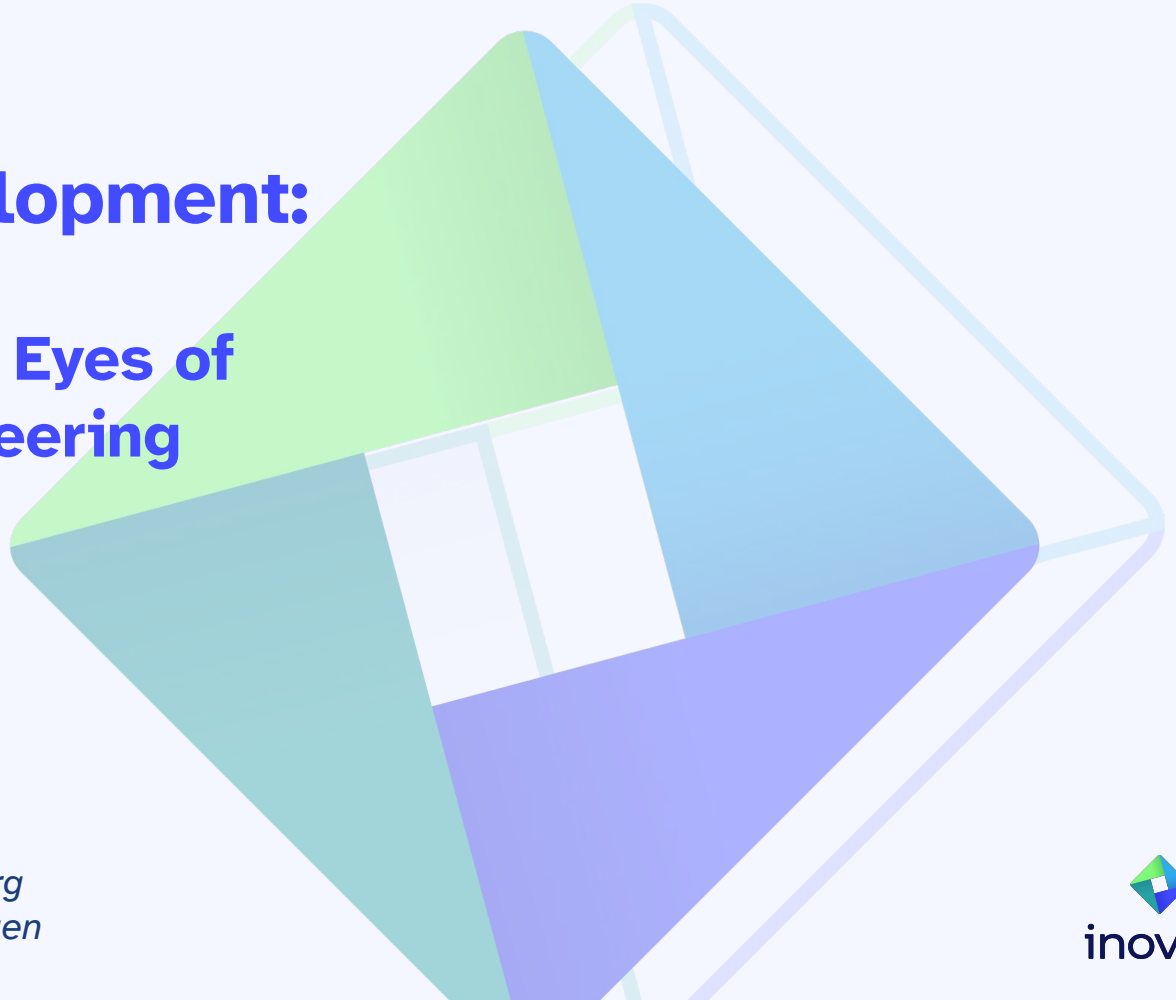


# Rethinking Embedded Development:

## Zephyr Through the Eyes of Model Driven Engineering

### **Team inovex**

*Karlsruhe · Köln · München · Hamburg  
Berlin · Stuttgart · Pforzheim · Erlangen*



# Dr. Tobias Kästner



Tobias Kaestner



@tobiaskaestner



@tobiaskaestner

Solution Architect Medical IoT

#FOSS4MEDICAL

- PhD in Physics (long ago)
- SW/System Architect since 15 years
  - mainly Medical Devices
- Trainer & Technical Consultant
  - SW-Architecture, Zephyr, Yocto
- In Love w/ Zephyr since 2016
  - realised several prototype projects for life-science R&D
  - Maintainer of TiacSys-Bridle Project
  - Participant Zephyr Safety-WG & Zephyr TSC
- Inovex Zephyr Project Silver Member since Nov 2024

# Agenda for today

- A very short 101 on MDSD
- The many DSLs of Zephyr
- The power of models

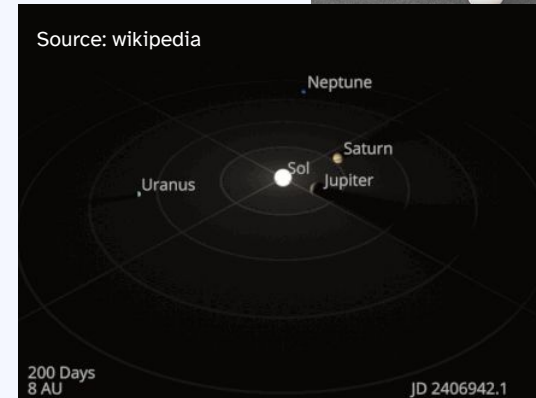
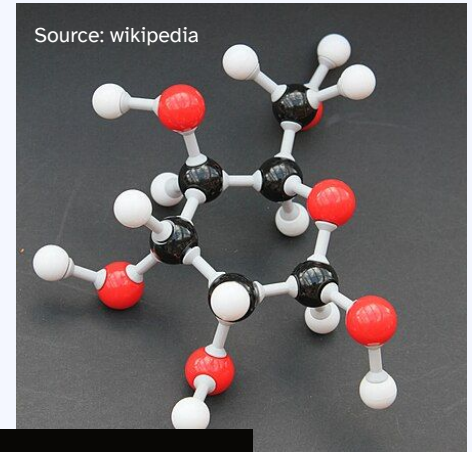


# A very short 101 on MDSD

# What is a model - and if, how many?

Models are sense-making devices to

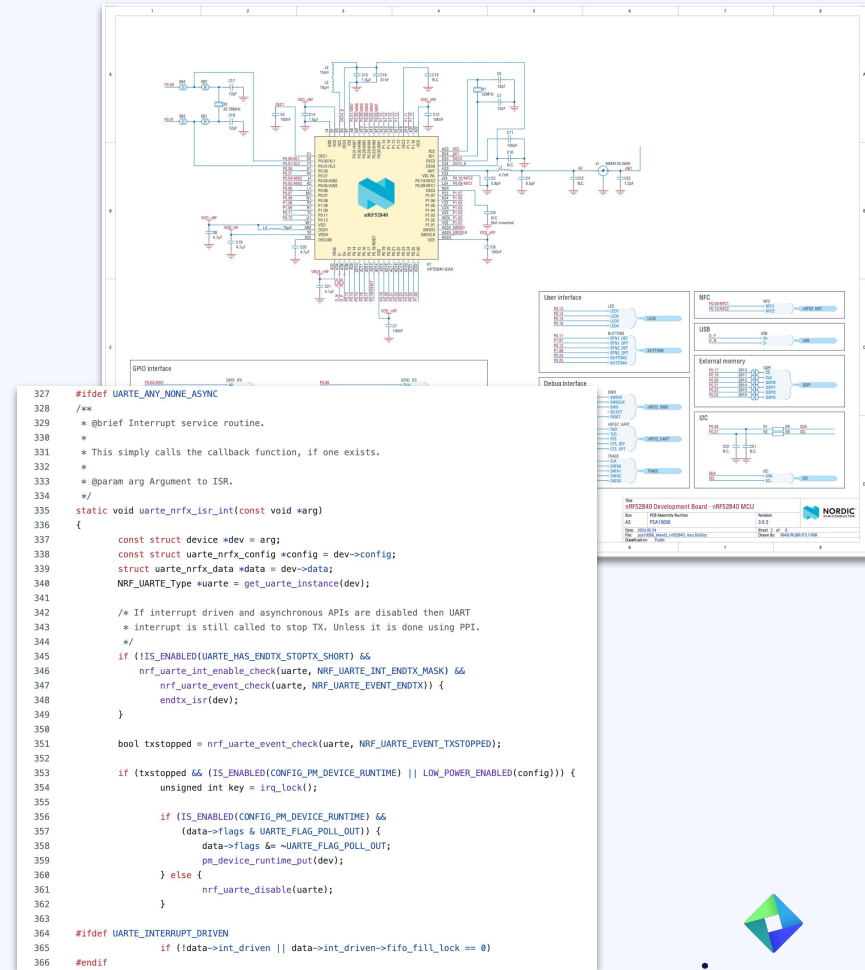
- encode information about the world
- and reason about its properties
- **communicate** our understanding &
- make predictions



# The many forms of models

Models are

- unavoidable
- abstractions
- domain-specific
- not guaranteed to agree with each other



The image displays three interconnected components related to the nRF52840 development board:

- Hardware Schematic:** A detailed circuit diagram of the nRF52840 microcontroller, showing its internal architecture, pin connections, and various peripheral components like memory and power management blocks.
- C Code Snippet:** A section of C code defining an interrupt service routine for the nRF52840. The code includes comments and logic for handling UART interrupts, checking for data, and managing the device's runtime state.
- Block Diagram:** A summary diagram of the development board's interfaces, including the User interface (LEDs, buttons), NFC, USB, External memory, and Debug interface (SWD, JTAG).

```
327 #ifdef UARTE_ANY_NONE_ASYNC
328 /*
329  * @brief Interrupt service routine.
330  *
331  * This simply calls the callback function, if one exists.
332  *
333  * @param arg Argument to ISR.
334  */
335 static void uarte_nrf_isr_int(const void *arg)
336 {
337     const struct device *dev = arg;
338     const struct uarte_nrf_config *config = dev->config;
339     struct uarte_nrf_data *data = dev->data;
340     NRF_UART_Type *uarte = get_uarte_instance(dev);
341
342     /* If interrupt driven and asynchronous APIs are disabled then UART
343      * interrupt is still called to stop TX. Unless it is done using PPI.
344      */
345     if (IS_ENABLED(UARTE_HAS_ENDTX_STOPTX_SHORT) &&
346         nrf_uarte_int_enable_check(uarte, NRF_UART_EVENT_ENDTX_MASK) &&
347         nrf_uarte_event_check(uarte, NRF_UART_EVENT_ENDTX)) {
348         endtx_isr(dev);
349     }
350
351     bool txstopped = nrf_uarte_event_check(uarte, NRF_UART_EVENT_TXSTOPPED);
352
353     if (txstopped && (IS_ENABLED(CONFIG_PM_DEVICE_RUNTIME) || LOW_POWER_ENABLED(config))) {
354         unsigned int key = irq_lock();
355
356         if (IS_ENABLED(CONFIG_PM_DEVICE_RUNTIME) &&
357             (data->flags & UARTE_FLAG_POLL_OUT)) {
358             data->flags & ~UARTE_FLAG_POLL_OUT;
359             pm_device_runtime_put(dev);
360         } else {
361             nrf_uarte_disable(uarte);
362         }
363     }
364 #ifdef UARTE_INTERRUPT_DRIVEN
365     if (!data->int_driven || data->int_driven->fifo_fill_lock == 0)
366 #endif

```

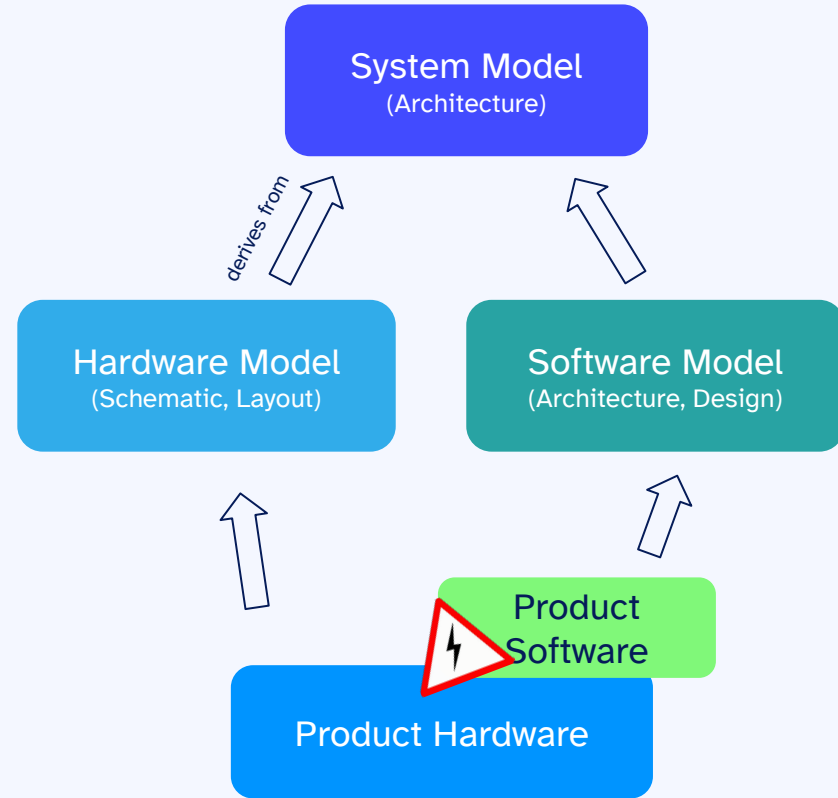
# Models in Embedded

Embedded Development comprises of

- System Development Domain
- Hardware Development Domain
- Software Development Domain

Key Challenges of Embedded Development

- how to make sure **models do align**
- propagate **changes** consistently
- deal with **implicit** models



# Models in Embedded

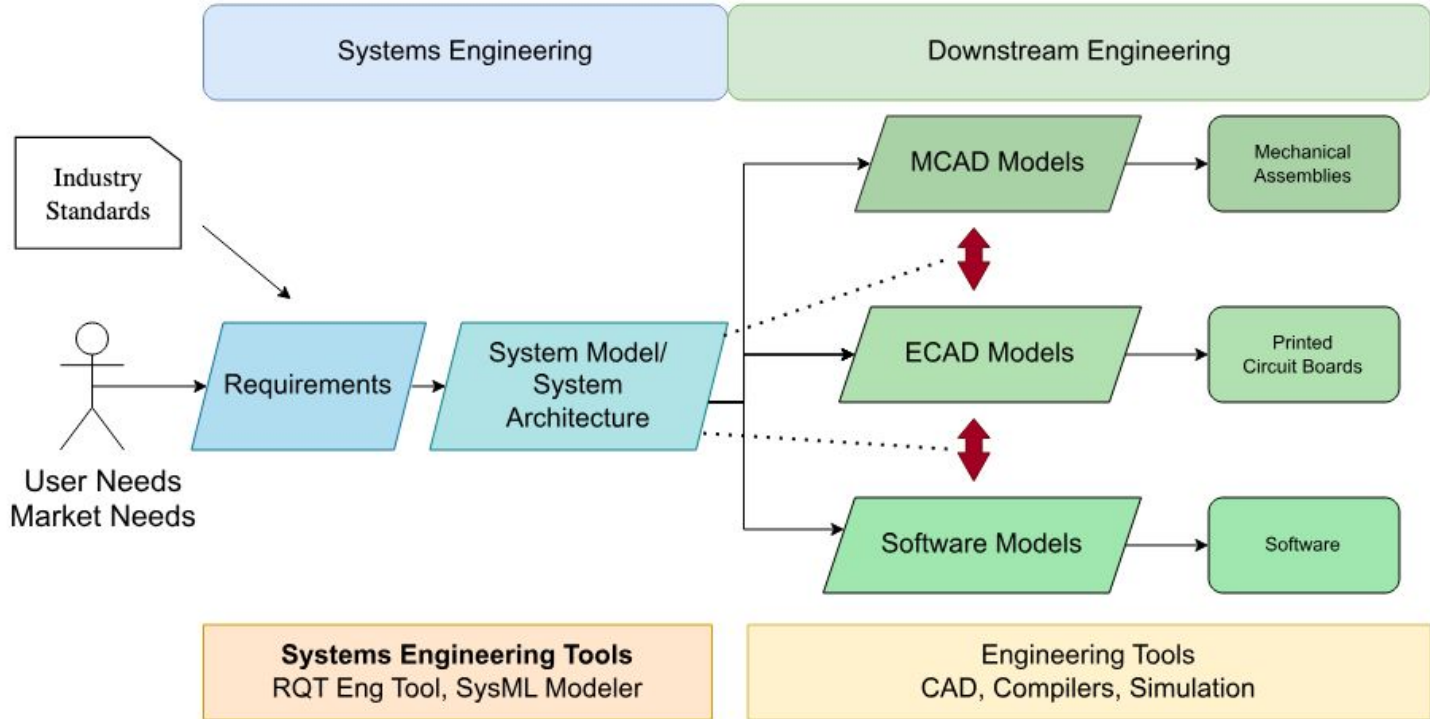
System Model  
(Architecture)

## Embedd

- Sysi
- Har
- Soft

## Key Cha

- how
- proj
- dea



Model Design



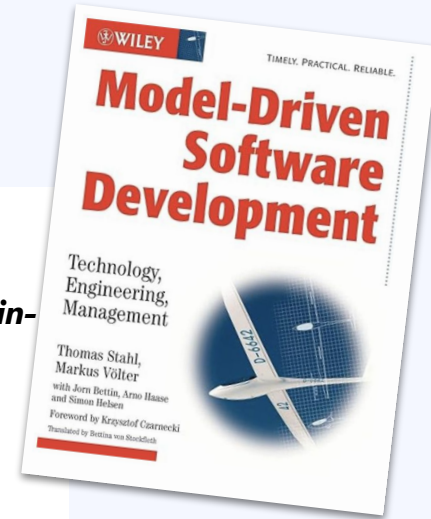
# Model Driven Software Development

“MDSO therefore aims to find **domain-specific abstractions** and make them accessible through formal modeling. This procedure creates a great potential for **automation** of software production, which in turn leads to **increased productivity**. Moreover, both the **quality and maintainability** of software systems increase.[ ... ] The adjective ‘driven’ in ‘Model-Driven Software Development’ [...] emphasizes that this paradigm assigns models a **central [...] role:** they are at least **as important as source code**.

To successfully apply the ‘domain-specific model’ concept, **three requirements** must be met:

- **Domain-specific languages** are required to allow the actual formulating of models.
- Languages that can express the necessary **model-to-code transformations** are needed.
- **Compilers, generators or transformers** are required that can run the transformations to generate code executable on available platforms.”

MDSO by Stahl and Voelker, 2006



# The many DSLs of Zephyr

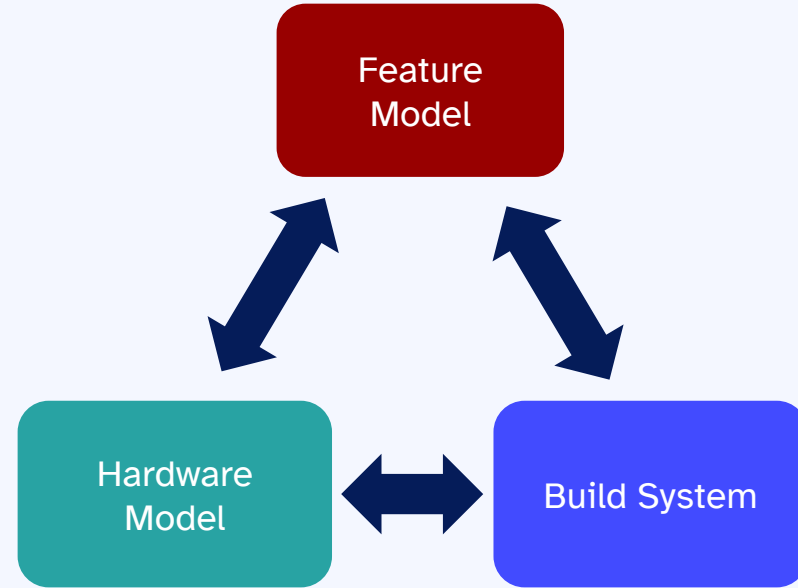
# Models in Zephyr

## 3 domain-specific models at play

**Feature Model:** select desired functionality

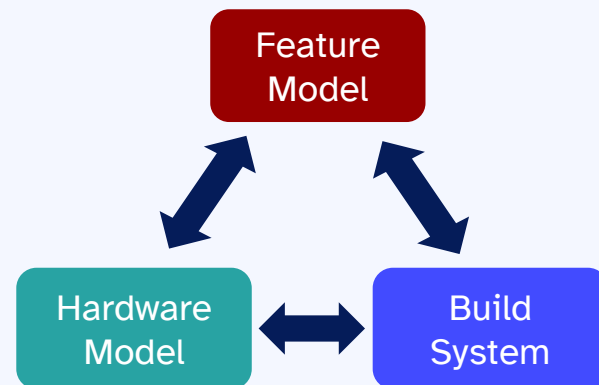
**Hardware Model:** to describe hardware properties

**Build System:** to describe build process



# Models in Zephyr

## 3 domain-specific models at play



```
west build -b nucleo_g474re samples/hello_world
```

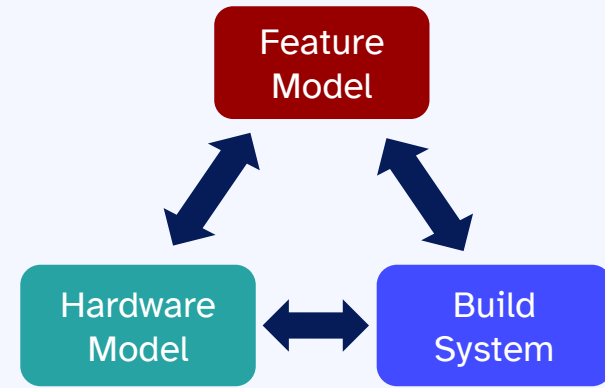
invoke  
CMake  
build  
system

select hardware  
& features  
<BOARD>.dts  
<BOARD>\_defconfig

select  
application  
& features  
prj.conf

# Models in Zephyr

## 3 domain-specific models at play



```
west build -b nucleo_g474re samples/hello_world
```

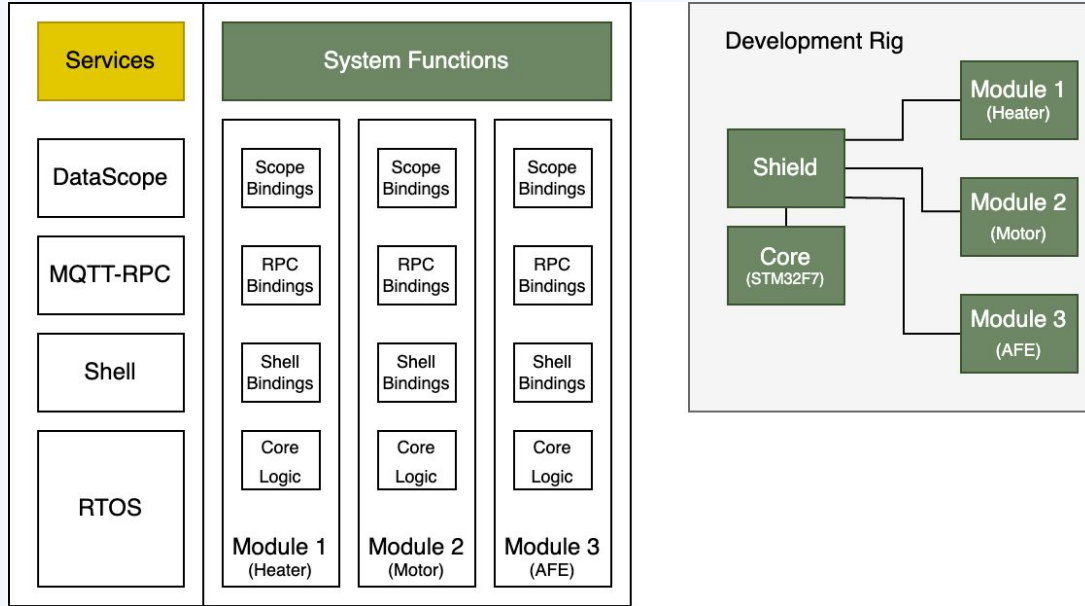
invoke  
CMake  
build  
system

select hardware  
& features  
<BOARD>.dts  
<BOARD>\_defconfig

select  
application  
& features  
prj.conf

**models are coupled and interact with each other !!!**

# Introducing ACME-NG



A couple of years ago ...

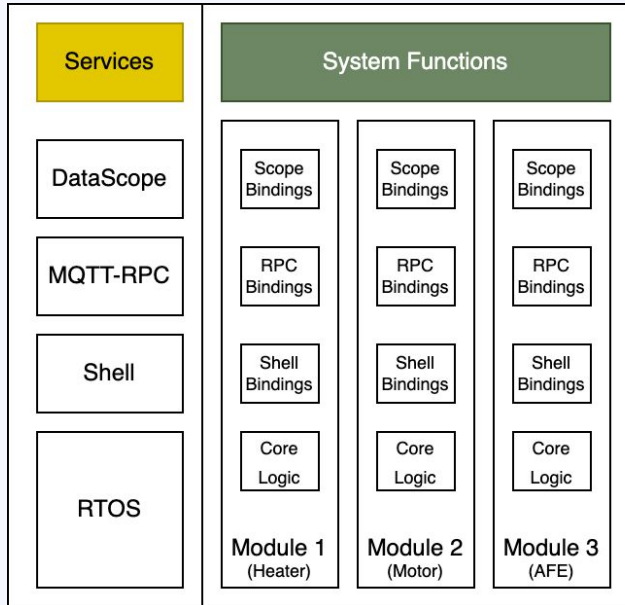
Goals of ACME : develop new type of high-performance test to diagnose Covid-19

**Iterative system design** - basic system functions known but specific details dependent on reagent chemistry developed simultaneously

**Time to Market** - extremely time-sensitive due to ongoing pandemic

**Supply-Chain-Risks** - Availability of HW components worsened dramatically during project time

# Modeling Software Features w/ Kconfig



```
1 menu "ACME Subsystems"
2
3 menu "Modules"
4     rsource "m_heater/Kconfig"
5     rsource "m_motor/Kconfig"
6     rsource "m_afe/Kconfig"
7 endmenu #Modules
8
9 menu "Core Services"
10     rsource "s_mqtt_rpc"
11     rsource "s_datascopes"
12 endmenu #Core Services
13 endmenu #ACME Subsystems
```

System Model  
(Architecture)

Software Model  
(Architecture, Design)

# Modeling Software Features w/ Kconfig

```
1 menu "ACME Subsystems"
2
3 menu "Modules"
4 rsource "m_heater/Kconfig"
5 rsource "m_motor/Kconfig"
6 rsource "m_afe/Kconfig"
7 endmenu #Modules
8
9 menu "Core Services"
10 rsource "s_mqtt_rpc"
11 rsource "s_datascopes"
12 endmenu #Core Services
13 endmenu #ACME Subsystems
```

```
1 menuconfig ACME_SUBSYS_HEATER # option to toggle the entire subsystem on/off
2 bool "Heater subsystem"
3 help
4 The Heater subsystem is responsible for measuring and controlling
5 the temperature.
6
7 if ACME_SUBSYS_HEATER
8
9 config ACME_SUBSYS_HEATER_THREAD_STACK_SIZE
10 int "Stack size of subsystem thread"
11 default 2048
12
13 config ACME_SUBSYS_HEATER_MQTT_RPC
14 bool "Enable MQTT-RPC bindings for $(subsys-str) subsystem"
15 depends on ACME_MQTT_RPC
16
17 config ACME_SUBSYS_HEATER_SHELL
18 bool "Enable shell bindings for $(subsys-str) subsystem"
19 depends on SHELL
20
21 config ACME_SUBSYS_HEATER_SCOPE
22 bool "Enable data scope bindings for $(subsys-str) subsystem"
23 depends on ACME_SCOPE #only selectable if datascopes is enabled
24
25 endif
```



# Modeling Software Features w/ Kconfig

Kconfig is a domain-specific language to describe software feature models

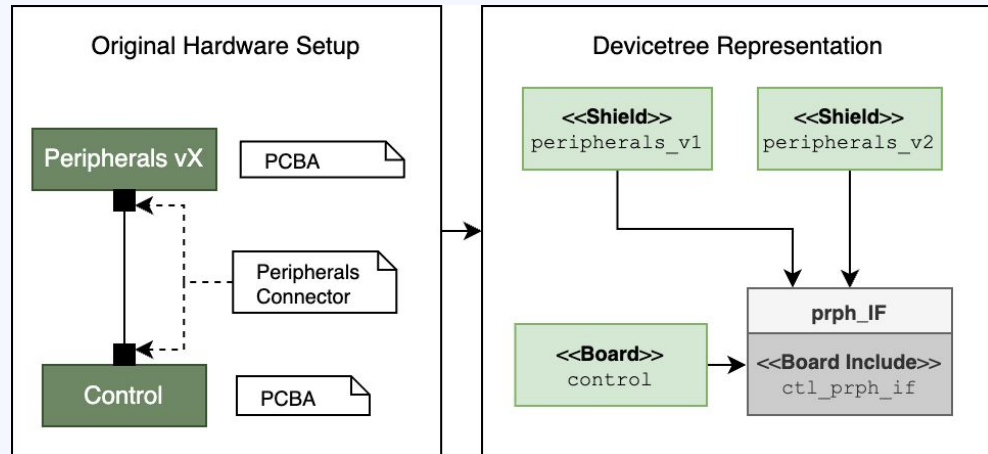
- features are typed & can relate to each other (**select**, **depend**, **imply**)
- models can be composed from smaller models (**[or] source**)
- models are transformed at build time into C language constructs



# Modeling Hardware Features w/ Devicetree

Devicetree is a domain-specific language to describe **hardware properties** which are **software relevant**

If used correctly, HW setups can be mapped faithfully to devicetree models including hardware interface aka interconnects



# Modeling Hardware Features w/ Devicetree

```
/ {  
    model = "Nordic nRF52840 DK NRF52811";  
    compatible = "nordic,nrf52840-dk-nrf52811";  
  
    aliases {  
        led0 = &led0;  
    };  
  
    leds {  
        compatible = "gpio-leds";  
        led0: led_0 {  
            gpios = <&gpio0 13 GPIO_ACTIVE_LOW>;  
            label = "Green LED 0";  
        };  
    };  
  
    gpio0: gpio@50000000 {  
        gpio-controller;  
        compatible = "nordic,nrf-gpio";  
        reg = < 0x50000000 0x200 0x50000500 0x300 >;  
        #gpio-cells = < 0x2 >;  
        status = "okay";  
        ...  
    };  
}
```

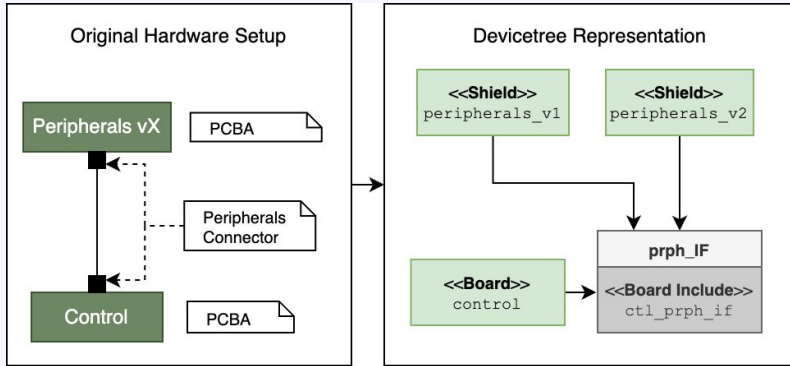
scripts/dts/gen\_defines.py

#include <generated/devicetree\_generated.h>

```
#include <zephyr/devicetree.h>  
  
#define LED0_NODE DT_ALIAS( led0)  
  
static const struct gpio_dt_spec led = GPIO_DT_SPEC_GET(LED0_NODE,  
gpios);  
  
int main(void)  
{  
    int ret;  
    bool led_state = true;  
  
    if (!gpio_is_ready_dt(& led)) {  
        return 0;  
    }  
  
    ret = gpio_pin_configure_dt(&led, GPIO_OUTPUT_ACTIVE);  
    ...  
}
```

# The power of models

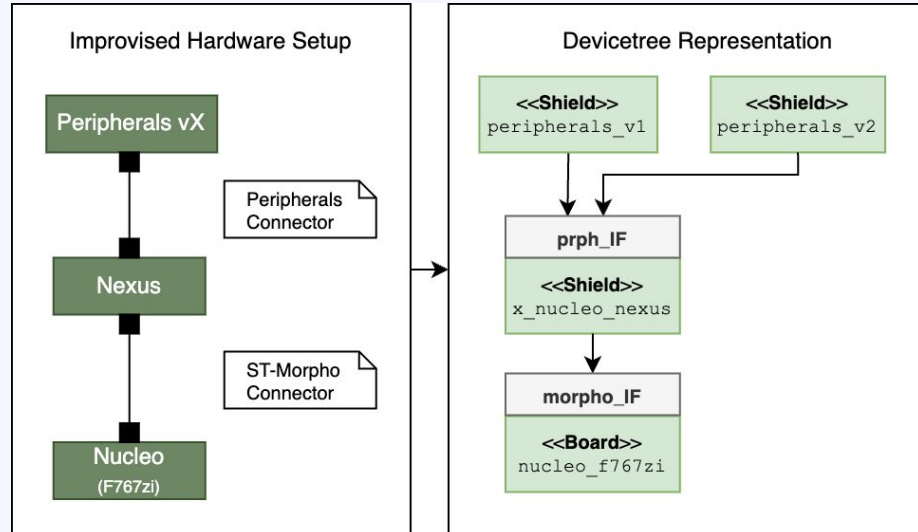
# When supply chains fall apart ...



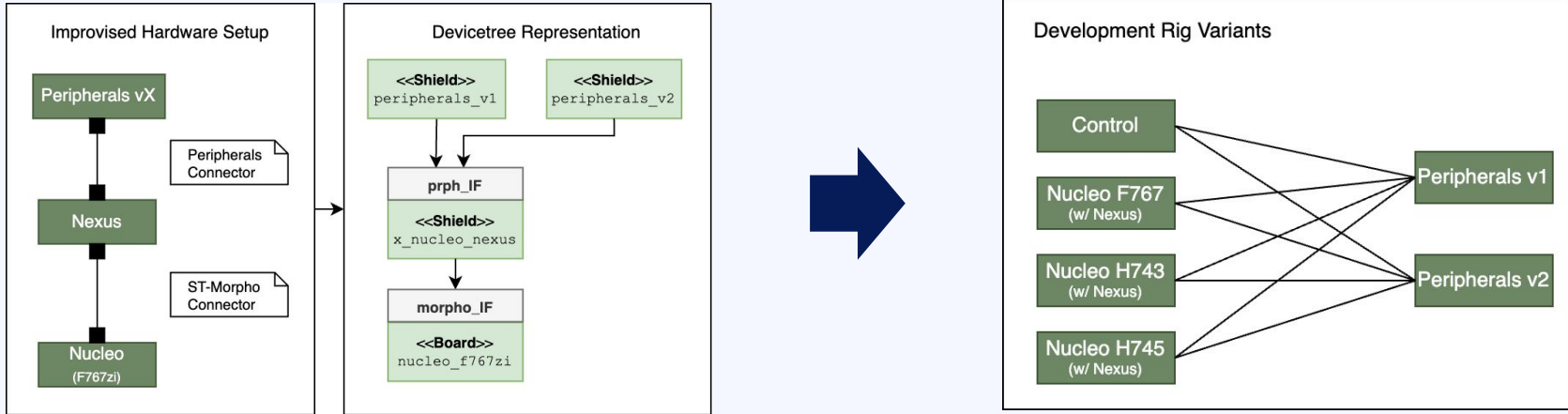
When ACME needed parts to make hardware the most, the parts had disappeared ...

... and all we could do, was to by existing devkit boards

devicetree models allowed us to **compensate for all HW changes** without touching a single line of source code



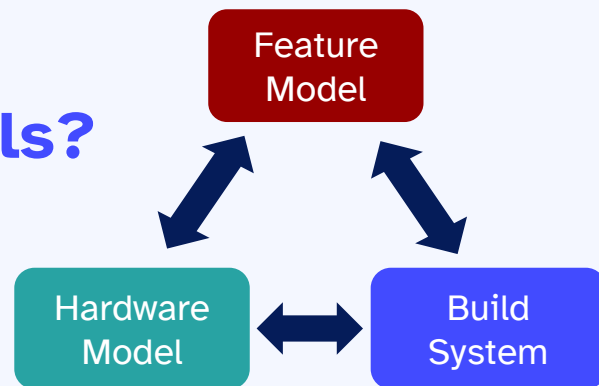
# When supply chains fall apart ...



```
west build -b core -shield peripherals_v1 acme_app
west build -b core -shield peripherals_v2 acme_app
```

```
west build -b nucleo_f767zi -shield x_nucleo_nexus -shield peripherals_v1 acme_app
west build -b nucleo_h743zi -shield x_nucleo_nexus -shield peripherals_v1 acme_app
```

## What makes us go that fast w/ models?



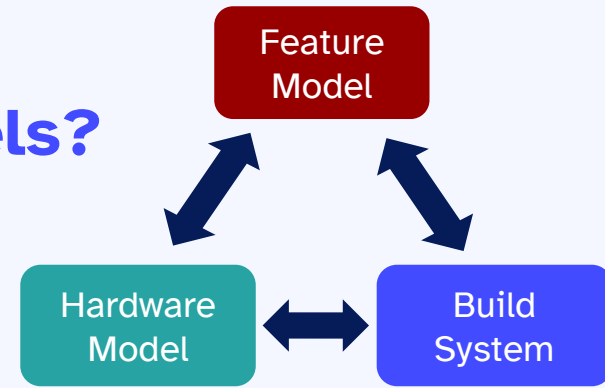
**Appropriate:** Devicetree, Kconfig and CMake established and mature

**Textual DSL:** easy to diff and version control, models as code

**Automated:** model transformations happen as part of software build process

**Transparent:** generated expressions consumable by standard C compiler

## What makes us go that fast w/ models?



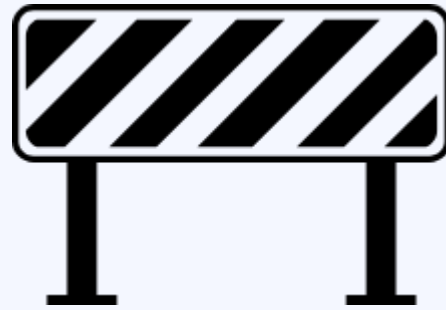
**Integrable:** Models can interact with each other to further increase usefulness

**Extensible:** Model languages can be extended with new constructs

**Open:** Underlying technologies open-source, no limitations to use or future development



# No model is perfect - never



Existing models as expressed by Kconfig, Devicetree and Zephyr CMake functions already extremely powerful ...

... however, not without limitations:

- missing abstractions: connectors (interface & multi-instance)
- missing concepts: multi-board setups (only via `--shield ... --shield ...` )
- missing composability: CS-lines of SPI devices

# Conclusion



- Zephyr showcases MDSD techniques, not through intent but by convergence
- Productivity gains partly explainable through this modeling approach
- Still plenty of space for improvements:
  - What other transformations could be looked at?
  - What other domains could be modeled?

# Thank You

## Zephyr Hands-On Trainings

starting 2025: Jan 22/23, Apr 02/03, Jul 02/03

Find out more

<https://www.inovex.de/de/training/zephyr-basic-training/>



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