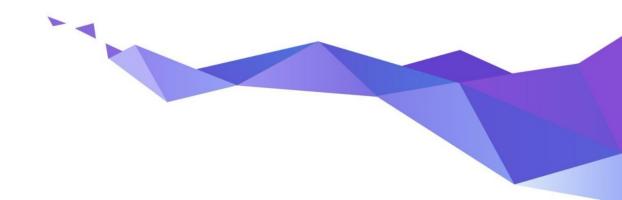


Zephyr Project Overview

A proven RTOS ecosystem, by developers, for developers



Use cases for a real-time OS







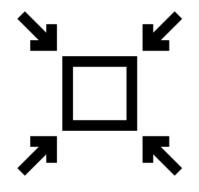


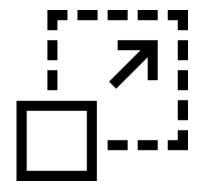












SMALL

yet

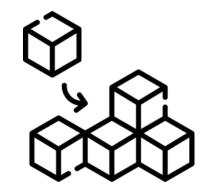
SCALABLE

- < 8KB Flash
- < 5KB RAM

from small sensor nodes

... to complex multi-core systems







yet



SECURE

Heavily customizable

Out-of-the-box support for 600+ boards and 100s of sensors

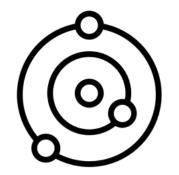
Built with safety & security in mind

Certification-ready

Long-term Support







OPEN-SOURCE

ECOSYSTEM

Permissively licensed (Apache 2.0)

Vendor-neutral governance

Vibrant community
Supported by major silicon vendors

Features overview



- Lightweight kernel & supporting drivers and services
- Portable, secure, power-efficient
- Highly connected
 - Bluetooth 5.0 & BLE
 - Wi-Fi, Ethernet, CANbus, ...
 - o IoT protocols: CoAP, LwM2M, MQTT, OpenThread, ...
 - USB & USB-C

Complete developer environment

- Toolchain and HAL management
- Logging, tracing, debugging,

- Emulation/Simulation
- Testing framework







Oticon More Hearing Aid



Lildog & Lilcat Pet Tracker



Livestock Tracker



Moto Watch 100



Samsung Galaxy Ring



Proglove



Adhoc Smart Waste



Google Chromebook



Framework laptop



Keeb.io BDN9



Hati-ACE



Safety Pod



BLiXT solid state circuit breaker



Aethero Deimos Satellite



PHYTEC Distancer



Laird Connectivity sensors & gateways



BeST pump monitoring



Vestas Wind Turbines



zephyrproject.org/products-running-zephyr





Discreet rechargeable hearing aid that gives you access to all relevant sounds

Oticon More supports the brain in making sense of sound and it is easy to operate with a double push button for volume and programme control. It features Bluetooth wireless technology for seamless connectivity with your favourite devices.



Bluetooth LE Low Power



zephyrproject.org/portfolio/oticon-more





Sustainable energy solutions

Vestas is the energy industry's global partner on sustainable energy solutions. We design, manufacture, install, and service onshore and offshore wind turbines across the globe, and with more than 164 GW of wind turbines in 87 countries, we have installed more wind power than anyone else. Through our industry-leading smart data capabilities and unparalleled more than 144 GW of wind turbines under service, we use data to interpret, forecast, and exploit wind resources and deliver best-in-class wind power solutions. Together with our customers, Vestas' more than 28,000 employees are bringing the world sustainable energy solutions to power a bright future.



CANbus

Industrial Control



zephyrproject.org/portfolio/vestas-wind-turbines





Thin, light, highperformance 13.5" notebook

A thin, light, high-performance 13.5" notebook that is also easy to repair, upgrade, and customize. The embedded controller firmware is a fork of the Zephyr version of chromium-ec, and is fully open source.

framework

Embedded Controller

USB / USB-C

Power Mgmt



zephyrproject.org/portfolio/framework-laptop-13-diy-edition-amd-ryzen-7040-series





Professional grade, digital tape measure

The T1 Tomahawk, the world's first, professional grade, digital tape measure enables tradespeople, across industries, to collect measurements faster and more accurately than ever before. A live view, OLED display, shows measurements of the tape measure, digitally, in both english and metric units. With a click of a button, measurements are saved to a side mounted e-paper display as well as sent over Bluetooth to connected devices.



Low Power
Sensing







Turns your wired sensors into IP67-rated battery-operated wireless nodes, providing robust and secure messaging

Ezurio's Sentrius™ BT610 I/O Sensor with Bluetooth 5 turns your wired sensors into IP67-rated battery-operated wireless nodes, providing robust and secure messaging. Leveraging our BL654 module, it provides full Bluetooth 5 capabilities, opening up industrial and equipment monitoring applications.



Bluetooth

Cellular

Connectivity Management

zephyrproject.org/portfolio/sentrius

App Framework

750+ supported boards... and growing

















ESP32

Sipeed HiFive1

nRF9160 DK

STM32F746G Disco

M5StickC PLUS









Arduino Nano 33



M0 LoRa



TDK RoboKit 1

BBC micro bit v2 Blue Wireless Swan









Microchip SAM E54 **Xplained Pro Evaluation Kit**



Raspberry Pi Pico



Altera MAX10



NXP i.MX8MP EVK



Adafruit Feather u-blox EVK-NINA-B3



docs.zephyrproject.org/latest/boards/

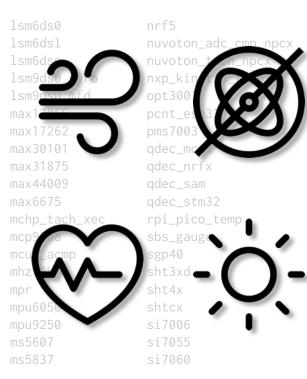
220+ Sensors Already Integrated

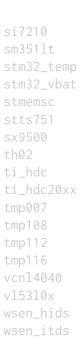


adt7420
adxl345
adx1362
adx1372
ak8975
amg88xx
ams_as5600
ams_iAQcore
apds9960
bma280
bmc150_magn
bme280
bme680
bmg160
bmi160
bmi270
bmm150
bmp388











Supported Hardware Architectures











Cortex-M, Cortex-R & Cortex-A

x86 & x86 64









32 & 64 bit



Vibrant Ecosystem









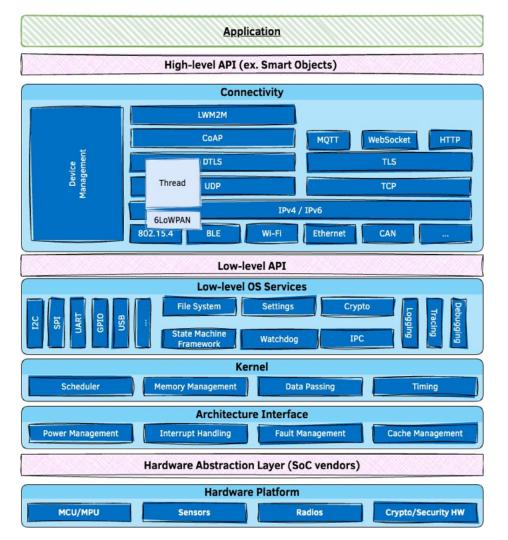






Firmwares & Libraries

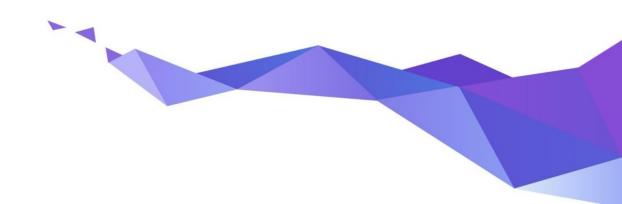
Architecture







Diving into Zephyr's features



Devicetree



Describe & **configure** the available hardware on the target system

Decouple the application from the hardware

+ **Kconfig** for all things configuration

```
docs.zephyrproject.org/latest/build/dts
```

```
&i2c1 {
    pinctrl-0 = <&i2c1_scl_pb8 &i2c1_sda_pb9>;
    pinctrl-names = "default";
    clock-frequency = <I2C_BITRATE_FAST>;
    status = "okay";
    1sm6ds1@6a {
        compatible = "st,lsm6dsl";
        reg = <0x06a >;
    };
    hts22105f {
        compatible = "st,hts221";
        reg = <0x5f >;
    };
    // ...
};
```

West meta-tool



Module Management

 Simplifies Versioning and integration of various modules/libraries in the build system

Build

- Extensible command-line interface
 - e.g. custom commands for specific board
 - Static code analysis, RAM/ROM reports

Connectivity Options

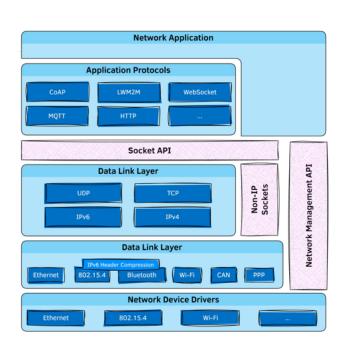


- Wide variety of communication protocols
 - o Ethernet, 802.15.4, Thread, LoRa, Bluetooth, CAN bus, ...
- **Core network protocols** like IPv6, IPv4, UDP, TCP, ICMPv4, and ICMPv6.
- **Security** (ex. TLS, DTLS, ...)
- Cloud integration using MQTT, CoAP and HTTP protocols
- Over-the-air updates
- Device management using OMA LwM2M 1.1 protocol

Native IP Stack



- Built from scratch, on top of Zephyr native kernel concepts
- Dual mode IPv4/IPv6 stack
 - DHCP v4, IPv4 autoconf, IPv6 SLAAC, DNS, SNTP
- Multiple network interfaces support
- Time Sensitive Networking support
- BSD Sockets-based API
- Supports IP offloading
- Compliance and security tested









Bluetooth 5.3 compliant • LE Controller • Host • Mesh • Bluetooth-SIG qualifiable USB 2.0 • USB-C • Device & Host • WebUSB

Zephyr USB Device Stack



- USB 2.0 & USB-C support
- Supports multiple MCU families (STM32, Kinetis, nRF, SAM,...)
- Supports most common devices classes: CDC, Mass Storage, HID, Bluetooth HCl over USB, DFU, USB Audio, etc.
- Tight integration with the RTOS
- Native execution support for emulated development on Linux
- WebUSB support

Power Management



- Goal: use as little power as possible
- Cross-platform (architecture / SoC agnostic)
- Tickless scheduler
- Handled by the kernel / Customizable by the user

Devicetree



Describe & **configure** the available hardware on the target system

Decouple the application from the hardware

```
&i2c1 {
    pinctrl-0 = <&i2c1_scl_pb8 &i2c1_sda_pb9>;
    pinctrl-names = "default";
    clock-frequency = <I2C_BITRATE_FAST>;
    status = "okay";
    1sm6ds1@6a {
        compatible = "st,lsm6dsl";
        reg = <0x06a >;
    };
    hts22105f {
        compatible = "st,hts221";
        reg = <0x5f >;
    };
    // ...
};
```



.dts file example

Secure boot / Device Management



- Leverage MCUboot as secure bootloader
- Application binary can be signed/encrypted
 - Can use hardware keys
- But also:
 - Downgrade prevention
 - Dependency checks
 - Reset and failure recovery
- Over-the-air (OTA) upgrades
 - OMA LwM2M, Eclipse hawkBit
 - Vendor offerings

Hardware security



Cryptography APIs

- Random Number Generation, ciphering, etc.
- Supported by crypto HW, or SW implementation (TinyCrypt)

• Trusted Firmware integration

- Firmware verification/encryption
- Device attestation
- Management of device secrets



Building on POSIX



Zephyr apps can run as native Linux applications

- Easier to debug/profile with native tools
- Connect to real devices using TCP/IP, Bluetooth, CAN
- Helps minimize hardware dependencies during the development phase

Re-use existing code & libraries by accessing Zephyr services through POSIX API

- Easier for non-embedded programmers
- Implementation is optimized for constrained systems
- Supported POSIX subsets: PSE51, PSE52, and BSD sockets



A real-time OS



Benchmark on Arm Cortex-M4F running at 120 MHz

Operation	Time
Thread create	2.5 µs
Thread start	3.6 µs
Thread suspend	3.3 µs
Thread resume	3.8 µs
Context switch (yield)	2.2 µs
Get semaphore	0.6 µs
Put semaphore	1.1 µs



Graphical User Interfaces



- Drivers available for various types of displays
 - o LCD
 - OLED
 - Touch panel displays
 - E-ink
- LVGL integration
- Support for video capture and output



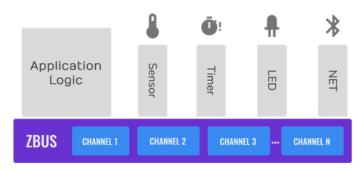
Inter-Process Communication



- Built-in kernel services (see table)
- IPC service
 - 1-to-1 or 1-to-many communications
 - No-copy API
- zbus (Zephyr Message Bus)
 - 1-to-1, 1-to-many, or many-to-many channel-based communications
 - Synchronous or asynchronous

Object	Bidirectional?	Data structure
FIFO	×	Queue
LIFO	×	Queue
Stack	×	Array
Message queue	×	Ring buffer
Mailbox	V	Queue
Pipe	×	Ring buffer

Data passing objects available in Zephyr kernel

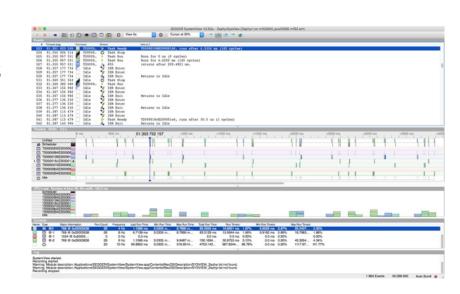


A typical zbus application architecture

Tracing & Debugging

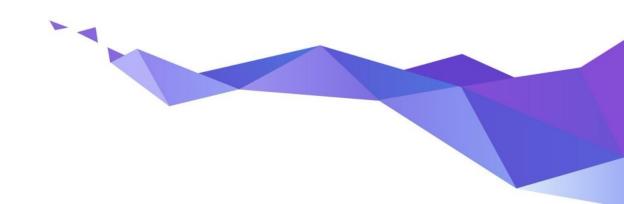


- Advanced logging framework
 - Multiple backends (UART, network, file system, ...)
 - Compile-time & runtime filtering
- **Tracing** framework
 - Visualize the inner-working of the kernel and its various subsystems
 - Object tracking (mutexes, timers, etc.)



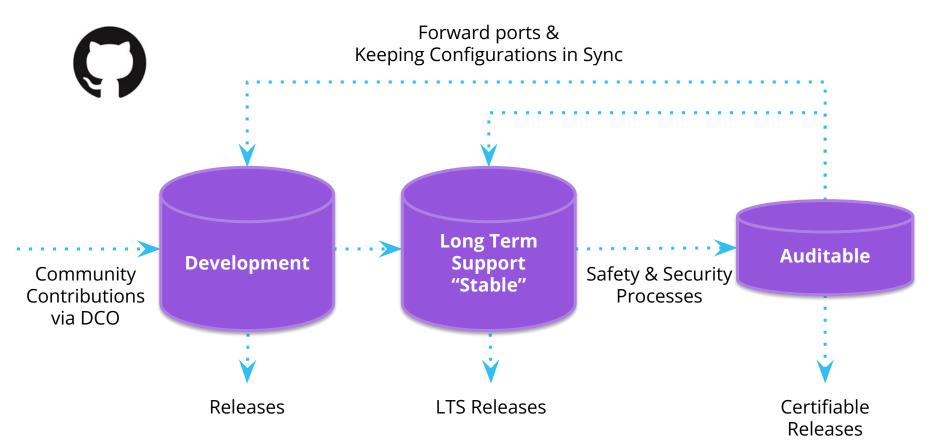


Safety & Security



Code Repositories





Long Term Support (Zephyr 2.7.x)



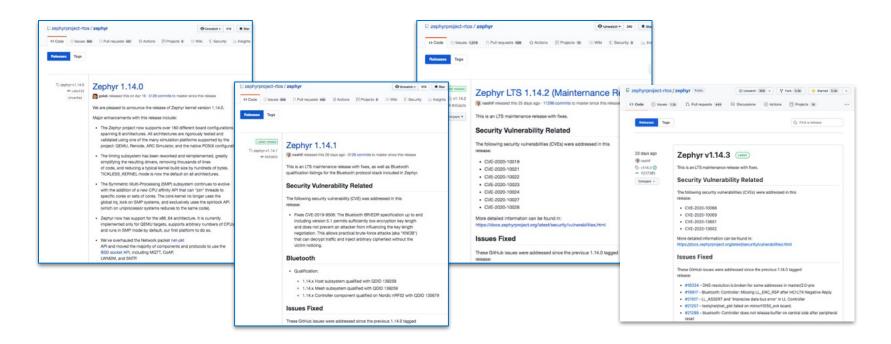
- Product Focused
- Current with latest Security Updates
- Compatible with new hardware
 - Functional support for new hardware is regularly backported
- Tested: Shorten the development window and extend the Beta cycle to allow for more testing and bug fixing
- Supported for 2+ years
- Doesn't include cutting-edge functionality



github.com/zephyrproject-rtos/zephyr/releases/tag/zephyr-v2.7.0

Long Term Support (LTS - 1.14)





Delivered bug fixes and latest security updates for 2 years!

Auditable



- An auditable code base will be established from a subset of the Zephyr OS LTS
- Code bases will be kept in sync
- More rigorous processes (necessary for certification) will be applied to the auditable code base.
- Processes to achieve selected certification to be:
 - Determined by Safety Committee and Security Committee
 - Coordinated with Technical Steering Committee



Project Security Documentation



- Project Security Overview
- Started with documents from other projects
- Built around Secure
 Development, Secure Design,
 and Security Certification
- Ongoing process, rather than something to just be accomplished



This is the documentation for the latest (main) development branch of Zephyr.

If you are looking for the documentation of previous releases, use the drop-

Open on GitHub # Report an issue with this page

Zephyr Security Overview

Docs / Latest » Security » Zephyr Security Overview

down menu on the left and select the desired version.

Introduction

This document outlines the steps of the Zephyr Security Subcommittee towards a defined security process that helps developers build more secure software while addressing security compliance requirements. It presents the key ideas of the security process and outlines which documents need to be created. After the process is implemented and all supporting documents are created, this document is a top-level overview and entry point.

Overview and Scope

We begin with an overview of the Zephyr development process, which mainly focuses on security functionality.

In subsequent sections, the individual parts of the process are treated in detail. As depicted in Figure 1, these main steps are:

- Secure Development: Defines the system architecture and development process that ensures adherence to relevant coding principles and quality assurance procedures.
- Secure Design: Defines security procedures and implement measures to enforce them. A security architecture of the system and relevant sub-modules is created threats are identified and countermeasures designed. Their

Software Supply Chain



- Zephyr ships an SBOM (Software Bill of Materials) with each release
- Downstream consumers can leverage built-in tools to, in turn, generate source & build SBOMs for their deliverables

```
[...]
FileName: ./zephyr/zephyr.elf
SPDXID: SPDXRef-File-zephyr.elf
FileChecksum: SHA1: e74cebcac51dabd799957ac51e4edcd32541103d
[...]
Relationship: SPDXRef-File-zephyr.elf GENERATED_FROM SPDXRef-File-dev-handles.c
Relationship: SPDXRef-File-zephyr.elf GENERATED_FROM SPDXRef-File-isr-tables.c
Relationship: SPDXRef-File-zephyr.elf STATIC_LINK SPDXRef-File-libapp.a
Relationship: SPDXRef-File-zephyr.elf STATIC_LINK SPDXRef-File-libzephyr.a
Relationship: SPDXRef-File-zephyr.elf STATIC_LINK SPDXRef-File-libisr-tables.a
Relationship: SPDXRef-File-zephyr.elf STATIC_LINK SPDXRef-File-libkernel.a
[...]
```

Automating SBOM Generation During Build! Zephyr



- Create a build directory with CMake file API enabled
- Build project with "build metadata" enabled
- Compute SBOM(s)

```
west spdx --init -d BUILD_DIR
west build -d BUILD_DIR -- -DCONFIG_BUILD_OUTPUT_META=y
west spdx -d BUILD_DIR
```



SBOM for the **Zephyr source files** actually used by your application zephyr.spdx

SBOM for the source files of your **application** app.spdx

SBOM for all the build objects, inc. of course your final image build.spdx

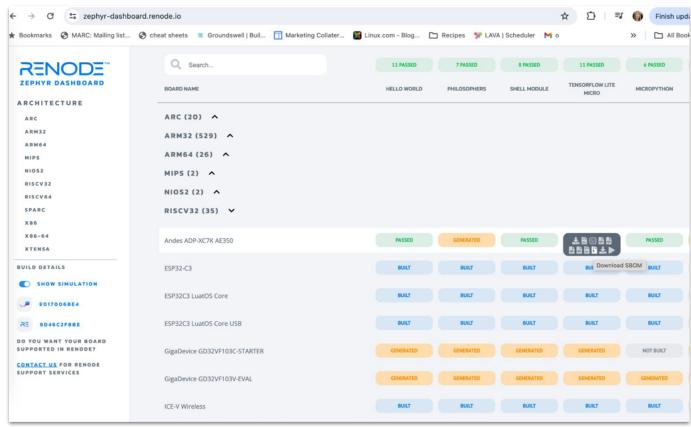
SBOM's at Scale...Automatically



708 boards

13 apps

All BUILT, PASSED, GENERATED have **3 SBOM**s available to download & inspect

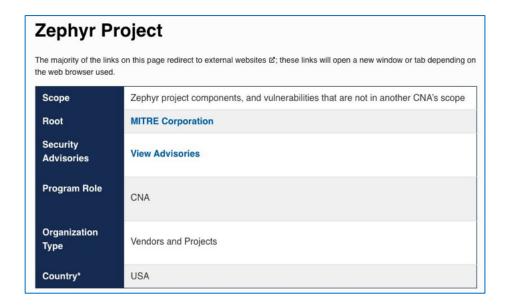


Source: https://zephyr-dashboard.renode.io/

CVE Numbering Authority



- Registered with MITRE in 2017
 - We issue our own CVEs
- Zephyr Project Security Incident Response Team (PSIRT)
 - Volunteers from the Security
 Subcommittee led by the Zephyr
 Security Architect.

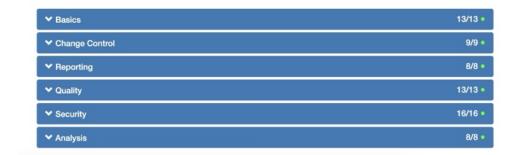


OpenSSF Gold Badge



- Core Infrastructure Initiative
 Best Practices Program
- Awards badges based on "project commitment to security"
- Mostly about project infrastructure: is project hosting, etc following security practices
- Gold status since Feb, 2019





Vulnerability Alert Registry

Zephyr®

- For an embargo to be effective, product makers need to be notified early so they can remediate
- The project aims at fixing issues
 within 30 days to give vendors 60
 days before publication of
 vulnerability







Zephyr PSIRT: Remediation and Response



Advisory Issued by project on 20201208:

- Zephyr current release (2.4) does not use Fnet or other stacks.
- The Zephyr LTS release 1.14 contains an implementation of the TCP stack from Fnet.

Of the vulnerabilities reported in Fnet, 2, <u>CVE-2020-17468</u>, and <u>CVE-2020-17469</u>, are in the IPv6 Fnet code, one, <u>CVE-2020-17467</u>, affects Link-local Multicast Name Resolution LLMNR), and 2, <u>CVE-2020-24383</u>, and <u>CVE-2020-17470</u> affect DNS functionality.

None of the affected code has been used in the Zephyr project, while 1.14 does use the Fnet TCP, it does not use the affected IPv6, DNS or LLMNR code.



- how to mitigate those.

 AMNESSA:33 is the first study we have published under Project Memoria. In this study, we discuss the results of the security analysis of seven open source TCP/IP stacks and report a bundle of 33 new walnersbillies fraud in lines of the seven analysed stacks that we used to main! of the seven analysed stacks that we used to main! or
- Four of the valuerabilities in ANNESIA-33 are critical, with potential for remote code execution on certain devices. Exploiting these valuerabilities could allow an atticken to take control of a device, this using it is an entity point on a retwork for internet connected devices, as a pivot point for lateral inversement, as a persistence point on the target evitors for as the first larget of an attack. For enterprise explantations, this means they use at increased risk of frainty fine retwork compounded or having realizous actions inferenties their business.

T 150+
VENDORS AFFECTED

forescout.com/amnesia33/ research@fores

research@forescout.com

tol fee 1-866-377-877

Zephyr Security Summary









<u>Documented secure</u> <u>coding practices</u>

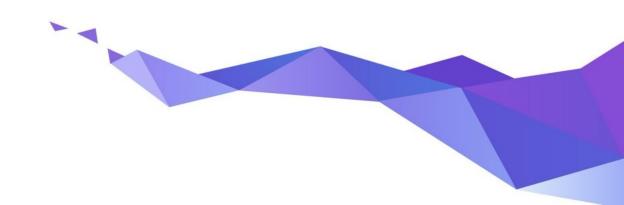
Vulnerability response criteria publicly documented

Weekly Coverity scans
MISRA scans

SBOM generation



Certification



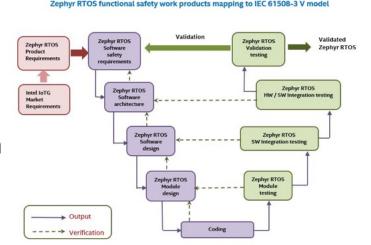
Compliant Development: V-model



It is difficult to map a stereotypical open-source development

to the V-model

- Specification of features
- Comprehensive documentation
- Traceability from requirements to source code
- Number of committers and information known about them



⇒ Provide the evidences that open source developers can map to compliance and meet all requirements

Safety Collateral Proposal



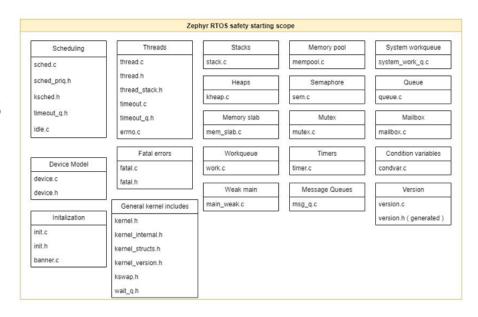
Artifacts	Type of Doc	Owner	Work in progress Visibility
Plans	Category		
Safety Development Plan	Plan/Process	Safety Committee	Public - Project Docs
Safety Assesment Plan	Plan/Process	FSM	Safety Committee Github
Verification / Validation / Integration Test Plan	Plan/Process	Testing WG	Public - Project Docs
Software Development Plan	Plan/Process	TSC	Public - Project Docs
Configuration and Change Management Plan	Plan/Process	TSC	Public - Project Docs
Coding Guideline	Plan/Process	TSC	Public - Project Docs
Tools Documentation	Plan/Process	TSC	Public - Project Docs
Specifications	Category		
Safety Scope Definition	Spec.	Safety Committee	Safety Committee Github
Safety Software Requirement Specification (SRS) **	Spec.	Safety Committee	Safety Committee Github
Safety Software Architecture and Interface Specification (SAIS) **	Spec.	Safety Committee	Safety Committee Github
Safety Software Component Design Specification (SMDS) **	Spec.	Safety Committee	Safety Committee Github
Safety Software Component Test Specification (SMTS) **	Spec.	Safety Committee	Safety Committee Github
Safety Software Integration Test Specification (SITS) **	Spec.	Safety Committee	Safety Committee Github
Safety Software Test Specification (STS) **	Spec.	Safety Committee	Safety Committee Github
Sources	Category	Carety Committee	carry committee cimas
Source Code	Source	TSC	Public
- Coding Guideline Compliance	Source	TSC	Public
Project Documentation	Source	TSC	Public
- Software Requirement Specifications	Spec	TSC	Public
- Software Architecture and Interface Specification	Spec	TSC	Public
- Software Component Design Specification	Spec	TSC	Public
	Source	TSC	Public
Project Testing - Software Component/Unit Test Specification	Spec	TSC	Public
		TSC	Public
- Software Integration Test Specification	Spec	TSC	Public
- Software Test Specification - Tests	Spec	TSC	Public
	Source	150	Public
Reports	Category		
Code Review Report (pre-merge)	Report	TSC	Public
Code Change Test Report (post-merge)	Report	Testing WG	Public
Test Coverage Report	Report	Testing WG	Public
Coding Guideline Compliance Report	Report	Safety WG & Security WG	Public
Traceability Report	Report	Safety WG	Public
Tools Classification	Report	Safety Committee	Public
Tools Validation	Report	Safety Committee	TBD (based on specific tools)
Fault Injection Test Report	Report	Safety Committee	Safety Committee
Safety Traceability Report (for Safety Scope) **	Report	Safety Committee/FSM	Safety Committee
Safety Test Coverage Report (for Safety Scope) **	Report	Safety Committee/FSM	Safety Committee
Safety Analysis (e.g., FMEA)	Report	FSM	Safety Committee
Manuals	Category		
Software User Manual	Manual	TSC	Public
Safety Manual	Manual	FSM	Safety Committee
Certificates			
All safety certificates	Certificate	Safety Committee	N/A

- Requirement definition, Source
 Code & Test linkage are public; and developed in open using <u>strictdoc</u>
- The set of requirements (and associated traceability) that are applicable to safety scope is managed by the safety committee.
- Other project artifacts have owners designated.

Initial certification focus



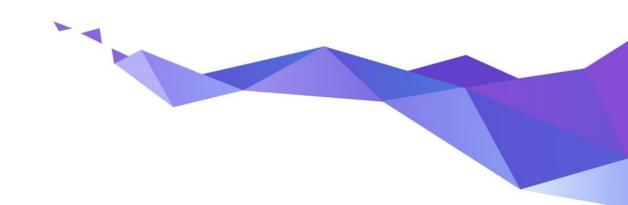
- Start with a limited scope of kernel and interfaces
- Initial target is IEC 61508 SIL 3 / SC 3 (IEC 61508-3, 7.4.2.12, Route 3s)
- Option for 26262 certification has been included in contract with certification authority should there be sufficient member interest



Scope can be **extended** to include **additional components** with associated **requirements** and **traceability** as determined by the safety committee



Ecosystem & Governance



Zephyr Project: Platinum Members

























Zephyr Project: Silver Members























































Vibrant Ecosystem















Firmwares & Libraries

Ecosystem // **Dev Tools**











IDE



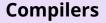






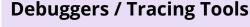






















Emulation / Simulation





Ecosystem // Training & Consulting







Training & Consulting



Firmwares & Libraries



Training















Services & Consulting













Ecosystem // Firmwares & Libraries







Training & Consulting



Firmwares & Libraries



Security





TinyML







Language runtimes











Others











Ecosystem // Apps & Middlewares





Remote Management















Robotics



Ecosystem // **Developer Tools**











IDE















Compilers





Debuggers / Tracing Tools











Emulation / Simulation





Ecosystem // Training & Consulting







Training & Consulting



Firmwares & Libraries



Training











Services & Consulting















Ecosystem // Firmwares & Libraries







Training & Consulting



Firmwares & Libraries



Security







TinyML





Language runtimes











Others











Ecosystem // Apps & Middlewares











Remote Management















Graphical Interfaces







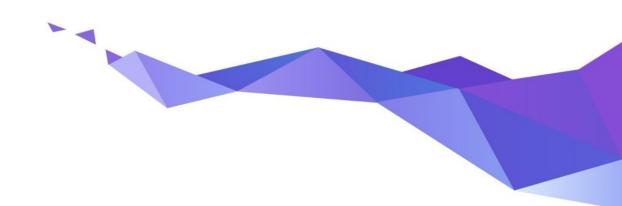


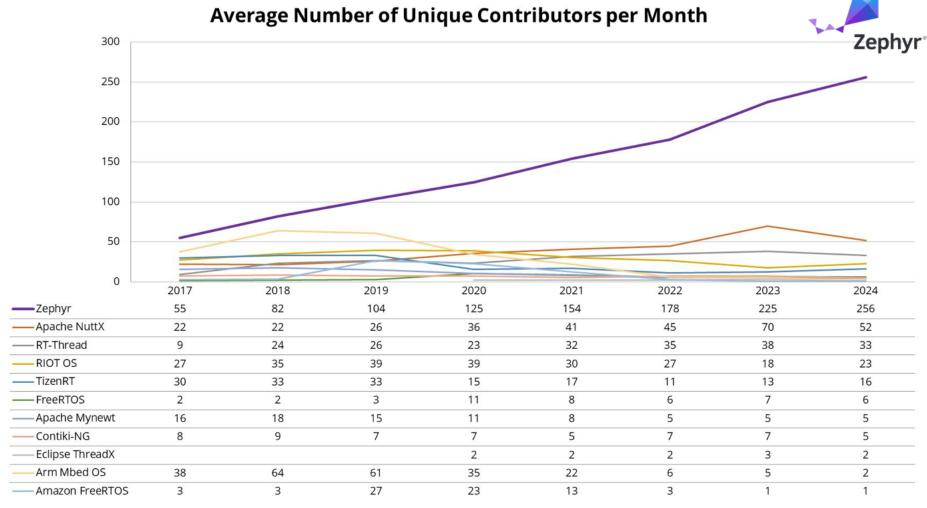
Robotics

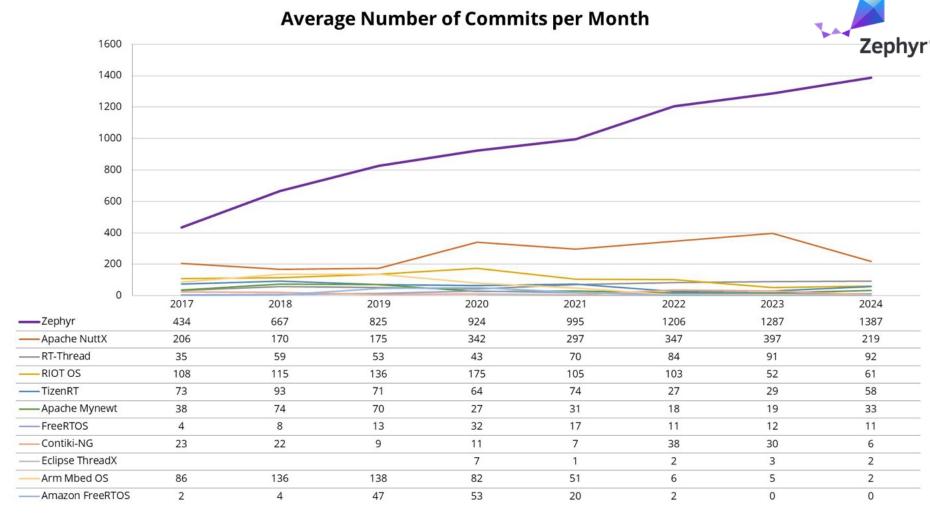


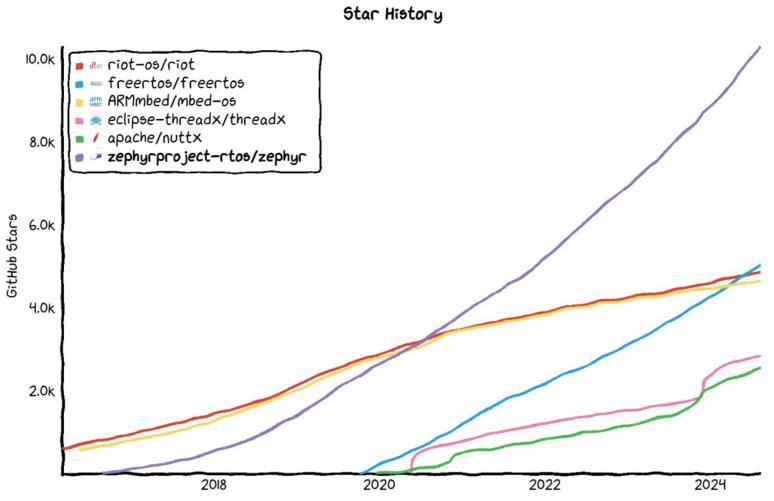


Zephyr in the RTOS landscape









Zephyr®

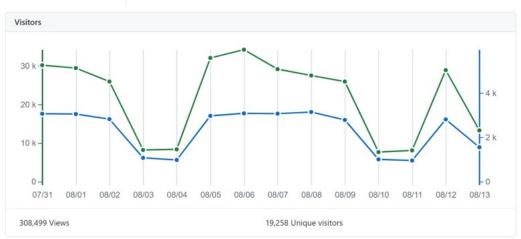
GitHub Clones & Unique Visitors





 $2024-07-31 \rightarrow 2024-08-13$

~186 unique clones per day ~1375 unique visitors per day



Getting started – Important links



- Check out the official <u>Getting Started Guide</u>
- Dig into the hundreds of code samples
- Check the catalog of 100s of available Devicetree bindings
 - No driver for your HW? Chances are a similar driver already exists and writing one is not as hard or daunting as you would think!
- Reach out to the community on **Discord**

Zephyr Participation Information





zephyrproject.org



github.com/zephyrproject-rtos



lists.zephyrproject.org



chat.zephyrproject.org





zephyrproject.org

